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PTO/SB/08A (10-01)

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Complete if known Substitute for form 1449/PTO 10/672,961 **Application Number** INFORMATION DISCLOSURE Filing Date September 26, 2003 Glenn J. Leedy First Named Inventor STATEMENT BY APPLICANT Art Unit 2822 **Examiner Name** Monica Lewis (use as many sheets as necessary) Sheet **Attorney Docket Number** ELM-2 Cont. 4

	U.S. PATENT DOCUMENTS								
Examiner	Cite No.1	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Documents	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear				
initials*		Number - Kind Code <sup>2</sup> (if known)							
		4,954,875	09-04-90	Clements					
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	FOREIGN PATENT DOCUMENTS									
Examiner initials	Cite No.1	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Documents	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>				
		Country Code <sup>1</sup> - Number <sup>2</sup> - Kind Code <sup>2</sup>								
		EP 0 238 089 A2	09-23-87	Fujitsu Ltd.						
		EP 0 703 618 A1	03-27-96	FRAUNHOFER- GESELLSCHAFT ZUR FÖRDERUNG DER ANGEWANDTEN FORSCHUNG E.V.						
		EP 0 703 619 A1	03-27-96	FRAUNHOFER- GESELLSCHAFT ZUR FÖRDERUNG DER ANGEWANDTEN FORSCHUNG E.V.						

NON PATENT LITERATURE DOCUMENTS						
Examiner initials	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published				
		Mitsumasa Koyanagi et al., "Design of 4-KBIT X 4-layer Optically Coupled Three-Dimensional Common Memory For Parallel Processor System," IEEE Journal of Solid-State Circuits, Vol. 25. No. 1, February 1, 1990.				

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.